

ABSTRACT OF THE DISCLOSURE

A circuit for reducing standby leakage in a memory unit contains a capacitive divider coupled to the memory unit so as to generate a voltage across the memory unit, which is adequate to retain memory values during one of a sleep state and a standby state. An inductive circuit for reducing standby leakage in a memory unit includes an inductive divider coupled to the memory unit so as to generate a voltage across the memory unit, which is adequate to retain memory values during one of a sleep state and a standby state.

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